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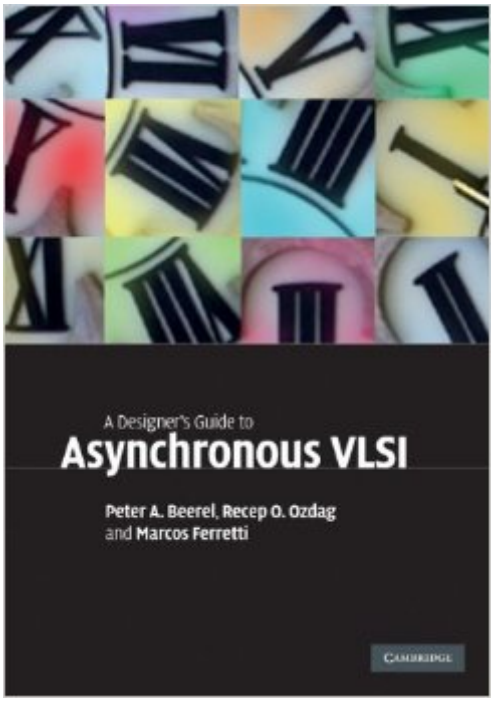
A Designer's Guide To Asynchronous VLSI



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Synopsis

Bypass the limitations of synchronous design and create low power, higher performance circuits with shorter design times using this practical guide to asynchronous design. The fundamentals of asynchronous design are covered, as is a large variety of design styles, while the emphasis throughout is on practical techniques and real-world applications.

Book Information

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Customer Reviews

I've read this book during a research I'm conducting in the field of asynchronous VLSI.This is a wonderfully organized book:The book begins with a short review on the field of Asynchronous VLSI - what have been done in the field so far, what are the difficulties and what are the advantages and disadvantages of implementing asynchronous VLSI.Later on, several asynchronous handshaking protocols are described and explained, accompanied with examples and waveforms which really helped me understand some pretty complex ideas. In addition, VerilogCSP is introduced, providing the reader with an easy way to practice and get an "hands-on" experience with asynchronous VLSI (no need for proprietry tools - any Verilog simulator will do!).The book continues with more complex concepts, like Micropipelines & QDI pipeline templates, which are explained very intuitively and are always followed by diagrams and exercises for the reader.All in all, I think it is a very good place to start with, if you are interested in the field of asynchronous VLSI. It helped me a lot and I really enjoyed reading it!I absolutely recommend it.

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